

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	51928	power near5 controller	USPA T; US-P GPUB	2004/09/1 7 20:12	
2	BRS	L2	7941	power near5 fault	USPA T; US-P GPUB	2004/09/1 7 20:13	
3	BRS	L3	6933	power near5 request\$3	USPA T; US-P GPUB	2004/09/1 7 20:14	
4	BRS	L4	21185	power near5 (command or instruction)	USPA T; US-P GPUB	2004/09/1 7 20:23	
5	BRS	L5	10469	power same (state near5 (stor\$3 or record\$3))	USPA T; US-P GPUB	2004/09/1 7 20:26	
6	BRS	L6	2178	(read\$3 near5 state) near10 power	USPA T; US-P GPUB	2004/09/1 7 20:29	
7	BRS	L7	64781	power near5 (remov\$3 or interrupt\$3)	USPA T; US-P GPUB	2004/09/1 7 20:30	
8	BRS	L8	1561	1 and 2	USPA T; US-P GPUB	2004/09/1 7 20:31	
9	BRS	L9	111	5 and 8	USPA T; US-P GPUB	2004/09/1 7 20:31	
10	BRS	L10	60	3 and 9	USPA T; US-P GPUB	2004/09/1 7 20:31	
11	BRS	L11	53	4 and 9	USPA T; US-P GPUB	2004/09/1 7 20:33	
12	BRS	L12	9	6 and 10	USPA T; US-P GPUB	2004/09/1 7 20:33	
13	BRS	L13	7	6 and 11	USPA T; US-P GPUB	2004/09/1 7 20:33	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	L14	9	12 or 13	USPA T; US-P GPUB	2004/09/1 7 21:22	
15	BRS	L15	1876	713/300.ccls. 713/310.ccls. 713/340.ccls.	USPA T; US-P GPUB	2004/09/1 7 21:23	
16	BRS	L16	14	10 and 15	USPA T; US-P GPUB	2004/09/1 7 21:24	
17	BRS	L17	14	11 and 15	USPA T; US-P GPUB	2004/09/1 7 21:24	
18	BRS	L18	16	16 or 17	USPA T; US-P GPUB	2004/09/1 7 21:24	
19	BRS	L19	13	18 not 14	USPA T; US-P GPUB	2004/09/1 7 22:04	
20	BRS	L20	4112	error near3 log	USPA T; US-P GPUB	2004/09/1 7 22:06	
21	BRS	L21	125	2 and 20	USPA T; US-P GPUB	2004/09/1 7 22:06	
22	BRS	L22	6	15 and 21	USPA T; US-P GPUB	2004/09/1 7 22:14	
23	BRS	L23	64781	power near5 (remov\$3 or interrupt\$3)	USPA T; US-P GPUB	2004/09/1 7 22:17	
24	BRS	L24	69792	2 or 23	USPA T; US-P GPUB	2004/09/1 7 22:18	
25	BRS	L25	727	15 and 24	USPA T; US-P GPUB	2004/09/1 7 22:18	
26	BRS	L26	148	5 and 25	USPA T; US-P GPUB	2004/09/1 7 22:18	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
27	BRS	L27	32	system adj power adj control adj network	USPA T; US-P GPUB	2004/09/1 7 22:20	
28	BRS	L28	2	26 and 27	USPA T; US-P GPUB	2004/09/1 7 22:22	
29	BRS	L29	30	27 not 28	USPA T; US-P GPUB	2004/09/1 7 22:49	
30	BRS	L30	6213	boot\$7 near5 (processor or computer)	USPA T; US-P GPUB	2004/09/1 7 22:50	
31	BRS	L31	64395	power\$3 near5 (processor or computer)	USPA T; US-P GPUB	2004/09/1 7 22:51	
32	BRS	L32	143	6 same (30 or 31)	USPA T; US-P GPUB	2004/09/1 7 22:52	
33	BRS	L33	54	5 and 32	USPA T; US-P GPUB	2004/09/1 7 22:52	
34	BRS	L34	13	15 and 33	USPA T; US-P GPUB	2004/09/1 7 23:08	
35	BRS	L35	37	24 and 33	USPA T; US-P GPUB	2004/09/1 7 23:09	
36	BRS	L36	28	35 not 34	USPA T; US-P GPUB	2004/09/1 7 23:34	
37	BRS	L37	13	33 not (34 or 36)	USPA T; US-P GPUB	2004/09/1 7 23:40	
38	BRS	L38	307	20 and 24	USPA T; US-P GPUB	2004/09/1 7 23:41	
39	BRS	L39	41	5 and 38	USPA T; US-P GPUB	2004/09/1 7 23:41	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
40	BRS	L40	33	39 and (30 or 31)	USPA T; US-P GPUB	2004/09/1 7 23:51	
41	BRS	L42	75	i adj o adj frame	USPA T; US-P GPUB	2004/09/1 7 23:53	
42	BRS	L41	1	cec adj frame	USPA T; US-P GPUB	2004/09/1 7 23:52	
43	BRS	L43	1	9 and 42	USPA T; US-P GPUB	2004/09/1 7 23:53	